

GLEN KAUKOLA

Riverside, Ca.

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SUMMARY

IT professional with a strong grasp of programming, operating systems, and computer networks.

SKILLS

Linux, Windows, *BSD, Cisco IOS, C, C++, C#, Fortran, Java, Python, Bash, HTML, CSS, SQL

EXPERIENCE

City of Riverside

Riverside, CA

Network Engineering Intern

February 2019 to June 2019

- Resolved, or escalated, incoming network support requests.
- Created and updated networking infrastructure documentation.
- Collaborated on team projects, including preparation tasks for a network equipment refresh.

Madsen, Kneppers & Associates

Walnut Creek, CA

Data Analyst

July 2014 to August 2015

- Developed custom reports and invoices for the Deltek Vision accounting system using C# and MS SQL.
- Assisted the infrastructure team as needed.

Quadrant

Los Angeles, CA

Senior Support Engineer

November 2012 to October 2013

- Worked with a team of support engineers to troubleshoot and resolve customer software and hardware issues.
- Worked alongside the provisioning team to quickly provision both customer owned and company servers.

University of California, Riverside

Riverside, CA

Programmer/Analyst IV

January 2007 to July 2008

- Maintained CE-CERT's Windows and Linux servers and network infrastructure.
- Virtualized CE-CERT's server infrastructure to improve both maintainence and the reliability of systems.
- Documented the important aspects of system operations.

University of California, Riverside

Riverside, CA

Programmer/Analyst II

June 2003 to December 2006

- Automated the Maintainence of a CentOS based high performance cluster.
- Developed and maintained analysis and manipulation tools for NetCDF and I/O API data using the GNU toolchain.
- Developed and maintained web pages for CE-CERT's Environmental Modeling Group.
- Streamlined application development using the Subversion revision control system and custom automated build tools.

EDUCATION

University of California, Riverside

Bachelor of Science, Computer Science, Emphasis on embedded systems.

Graduated 2005

- Dean's list, multiple quarters
- Reader, Department of Computer Science
- Lab assistant, digital electronics lab
- For a Synthesis of Digital Systems course, implemented cascading on a Synopsis synthesizable Intel 8237A DMA controller.
- Designed a TMS320C31 DSP simulator using C++, for a senior design project.
- Designed a MIPS r2000 processor and testbench using VHDL, along with associated lab exercises, for use in the undergraduate computer architecture curriculum.

HOBBIES & INTERESTS

Open source projects and communities including Fedora and KDE.